

IN THE CLAIMS

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Original) A panel driver for driving a liquid crystal display LCD device using a super twisted nematic STN mode, comprising:

at least one first means for receiving a plurality of source voltages and supplying a selected source voltage at an alternative among a plurality of driving power lines; and

at least one second means connected to the plurality of driving power lines for delivering the selected source voltage to a line.

2. (Original) The panel driver as recited in claim 1, further comprising:

a first driving power line; and

a second driving power line,

wherein first means outputs the selected source voltage to an alternative of a first and a second driving power lines.

3. (Original) The panel driver as recited in claim 2, wherein the second means includes a first transistor controlled by the first driving voltage control signal for transmitting the selected source voltage supplied at the first driving power line to the line; and

a second transistor controlled by the second driving voltage control signal for transmitting the selected source voltage supplied at the second driving power line to the line.

4. (Original) The panel driver as recited in claim 3, wherein the first means includes a plurality of voltage following means for delivering the plurality of source voltages; and a multiplexing means for receiving the plurality of source voltages from the plurality of voltage following means and outputting the selected source voltage to an alternative of the first and the second driving power lines in response to a power selecting signal.

5. (Withdrawn) The panel driver as recited in claim 3, wherein the first means includes a multiplexing means for receiving the plurality of source voltages and outputting the selected source voltage in response to a power selecting signal; and
a plurality of voltage following means for delivering the selected source voltage to an alternative of the first and the second driving power lines.

6. (Original) A panel driver for driving a liquid crystal display LCD device using a super twisted nematic STN mode, comprising:

at least one first supplying means for receiving a plurality of first source voltages and respectively supplying a first selected source voltage to an alternative of a first and a second driving power lines;

at least one second supplying means for receiving a plurality of second source voltages and respectively supplying a second selected source voltage to an alternative of a third and a forth driving power lines;

at least one first driving means for receiving the first selected source voltage and driving a segment line in response to a first and a second driving voltage control signals; and

at least one second driving means for receiving the second selected source voltage and driving a common line in response to a third and a forth driving voltage control signals.

7. (Original) The panel driver as recited in claim 6, wherein the first supplying includes a plurality of voltage following means for delivering the plurality of first source voltages; and

a multiplexing means for receiving the plurality of first source voltages from the plurality of voltage following means and outputting the first selected source voltage to an alternative of the first and the second driving power lines in response to a first power selecting signal.

8. (Original) The panel driver as recited in claim 7, wherein the second supplying includes

a plurality of voltage following means for delivering the plurality of second source voltages; and

a multiplexing means for receiving the plurality of second source voltages from the plurality of voltage following means and outputting the second selected source voltage to an alternative of the third and the forth driving power lines in response to a second power selecting signal.

9. (Withdrawn) The panel driver as recited in claim 6, wherein the first supplying includes

a multiplexing means for receiving the plurality of first source voltages and outputting the first selected source voltage in response to a first power selecting signal; and

a plurality of voltage following means for delivering the first selected source voltage to an alternative of the first and the second driving power lines.

10. (Withdrawn) The panel driver as recited in claim 9, wherein the second supplying includes

a multiplexing means for receiving the plurality of second source voltages and outputting the second selected source voltage in response to a second power selecting signal; and

a plurality of voltage following means for delivering the second selected source voltage to an alternative of the third and the forth driving power lines.

11. (Withdrawn) The panel driver as recited in claim 6, wherein the first driving means includes

a first line driver controlled by the first driving voltage control signal for transmitting the first selected source voltage supplied at the first driving power line to the segment line; and

a second line driver controlled by the second driving voltage control signal for transmitting the first selected source voltage supplied at the second driving power line to the segment line.

12. (Withdrawn) The panel driver as recited in claim 11, wherein the first line driver includes a first MOS transistor for delivering the first selected source voltage supplied at the first driving power line to the segment line, gate of the first MOS transistor being connected to the first driving voltage control signal.

13. (Withdrawn) The panel driver as recited in claim 12, wherein the second line driver includes a second MOS transistor for delivering the first selected source voltage supplied at the second driving power line to the segment line, gate of the second MOS transistor being connected to the second driving voltage control signal.

14. (Withdrawn) The panel driver as recited in claim 11, wherein the second driving means includes

a third line driver controlled by the third driving voltage control signal for receiving the second selected source voltage supplied to the third driving power line; and

a forth line driver controlled by the forth driving voltage control signal for receiving the second selected source voltage supplied to the forth driving power line.

15. (Withdrawn) The panel driver as recited in claim 14, wherein the third line driver includes a third MOS transistor for delivering the second selected source voltage supplied at the third driving power line to the common line, gate of the third MOS transistor being connected to the third driving voltage control signal.

16. (Withdrawn) The panel driver as recited in claim 15, wherein the forth line driver includes a forth MOS transistor for delivering the second selected source voltage supplied at the forth driving power line to the common line, gate of the forth MOS transistor being connected to the forth driving voltage control signal.